

1  
signals and the frequency of the program clock;  
maintaining a program clock value based on the program clock signals received at  
the decoder;  
determining if there is an absolute difference between the local clock value and  
the program clock value;  
if there is an absolute difference between the local clock value and the program  
clock value, then adjusting the frequency at which the local clock oscillates so that said  
difference approaches zero.

Sub  
g2  
2. (Thrice Amended) A method of synchronizing the frequency of a local clock of  
a digital data decoder with the frequency of a program clock, wherein the local clock oscillates at  
a local clock frequency, the method comprising the steps of:

D  
determining the difference between the local and program clock frequencies, then  
adjusting the frequency at which the local clock oscillates so that said difference approaches  
zero;

maintaining a local clock value based on the oscillations of the local clock;  
receiving clock time stamps at the decoder which specify program clock signals  
and the program clock frequency;

maintaining a program clock value based on the program clock signals received at  
the decoder;

determining if there is any difference between the local clock frequency and the  
program clock frequency;

determining if there is an absolute difference between the local clock value and  
the program clock value;

if there is either a difference between the local clock frequency and the program clock frequency or an absolute difference between the local clock value and the program clock value, then adjusting the frequency at which the local clock oscillates so that said difference approaches zero;

wherein the decoder includes hardware for adjusting the local clock frequency and a processor having a software program for adjusting the local clock frequency, and wherein the step of adjusting the frequency of the local clock includes the steps of:

using the hardware to adjust the local clock frequency until a threshold condition occurs; and

after the threshold condition occurs, using the processor to adjust the local clock frequency.

Sub  
E4

13. (Twice Amended) A system for synchronizing the frequency of a local clock of a digital data decoder with the frequency of a program clock, comprising:

means for determining if there is any difference between the local and program clock frequencies;

means for determining if there is an absolute difference between the local clock value and the program clock value; and

means for adjusting the frequency at which the local clock oscillates, when there is a difference between the local clock and the program clock frequencies, or an absolute difference between the local clock value and the program clock value, so that said difference approaches zero, wherein the means for adjusting includes